

ABSTRACT OF THE DISCLOSURE

A CMOS (Complementary Metal Oxide Semiconductor) operational amplifier includes: an amplifying unit for differentially amplifying signals inputted to
5 an inverting input terminal and non-inverting input terminal to reduce an input offset voltage and outputting the amplified signal to an output terminal; and a slew enhancing unit for increasing a slew rate of the amplified signal outputted to the output terminal on the basis of the input voltage value of the inverting input terminal and the input voltage value of the non-inverting input terminal.